

MEng Electronic and Information Engineering

This document provides a definitive record of the main features of the programme and the learning outcomes that a typical student may reasonably be expected to achieve and demonstrate if s/he takes full advantage of the learning opportunities provided. This programme specification is intended as a reference point for prospective students, current students, external examiners and academic and support staff involved in delivering the programme and enabling student development and achievement.

Programme Information			
Award(s)	MEng		
Programme Title	Electronic and Information Engineering		
Programme code	GH56		
Awarding Institution	Imperial College London		
Teaching Institution	Imperial College London		
Faculty	Faculty of Engineering		
Department	Department of Electrical and Electronic Engineering		
Associateship	City and Guilds of London Institute (ACGI)		
Mode and Period of Study	4 academic years full-time		
Cohort Entry Points	Annually in October		
Relevant QAA Benchmark Statement(s) and/or other external reference points	Honours Degrees in Engineering and Master's Degrees in Engineering		
Total Credits	ECTS:	252/276*	UK Credit: 492/540*
<i>*Total ECTS dependent on chosen Year 4 option module (Group Project or Industrial Placement) and dependent on number of optional modules from the Computing Department.</i>			
FHEQ Level	Level 7		
EHEA Level	2 nd cycle		
External Accreditor(s)	Institute of Engineering and Technology (IET)		
Specification Details			
Student cohorts covered by specification	2016-17 Entry		
Person responsible for the specification	Dr. K. Fobelets		
Date of introduction of programme	October 2013		

Date of programme specification/revision

August 2016

Description of Programme Contents

The goal of this course is the preparation of high quality graduates who will innovate beyond the current practises, whether in the electronic/electrical industry, in information technology, in research, or in the more commercial activities that attract many of our graduates. Our teaching and learning is supported by a variety of teaching methods covering large group sessions and 3-to-1 sessions. Members of staff are experts in their electrical engineering field and bring their research and industrial expertise into the classrooms.

Teaching on this programme is based on an integrated 3-year programme leading to the award of a BEng degree. The BEng course has been designed based upon a number of key principles:

- **Competence in the fundamental principles of mathematics and electrical/electronic engineering:** Students are expected to develop a firm grasp of the fundamental concepts and principles, and able to model complex systems analytically, to analyse and optimise these models.
- **Competence in computing:** Students are expected to acquire an extensive level of competence in both programming and using the latest computing technologies.
- **Pro-active learning:** Students are expected to learn how to learn by themselves and acquire the skill and discipline of lifelong learning.
- **Design Proficiency:** Students are expected to develop their ability to incorporate concepts into design of new products or processes, provide innovations.
- **Development of professional and transferrable skills:** Students are expected to learn how to work in groups, develop their abilities to communicate scientific/engineering ideas orally or in written form, and to develop general problem-solving skills.
- **Industrial perspectives:** Students are expected to participate in an industrial based project or related activities where possible.
- **Flexibility of provision:** We aim to provide students with a wide variety of options in the last year of the course in order to allow them to specialise in specific areas in electrical/electronic engineering and in computing.

Learning Outcomes

The Imperial Graduate Attributes are a set of core competencies that we expect students to achieve through completion of any Imperial College degree programme. The Graduate Attributes are available at: www.imperial.ac.uk/students/academic-support/graduate-attributes

Knowledge and Understanding of:

- Fundamental concepts and physical principles that underpin electronic and information engineering in the areas of circuits, systems, networks and the paradigms of high-level programming (declarative, imperative and object-orientated).
- Mathematical principles and techniques that underpin the analysis of engineering systems, circuits discrete processes and algorithms.
- Formal aspects of software engineering, including program specification, program reasoning and design methods.
- The application of the principles to engineering design and analysis.
- Software engineering and programming skills as appropriate to electrical and electronic engineering.

- The design and architecture of the CPU in portable devices, information theory and wireless protocols for connections to the internet, and in the operating systems and databases providing back-end support in the cloud.
- Role of business processes in engineering, including the commercial, societal and legal framework within which industry operates.
- Moral and ethical issues including professional conduct and intellectual property.

Intellectual Skills

- Performance of the analysis of engineering systems and circuits in order to solve qualitative and quantitative problems.
- Synthesise solutions using established methodologies.
- Adapt and apply methodologies to the solution of unfamiliar problems.
- Derive methodologies for unfamiliar problems.
- Practical application of theory using computer software and models.
- Analyse computing system design problems of varying types and specify problems and solutions in a suitable format.
- Demonstrate the skills necessary to plan, conduct and report a programme of independent research.

Practical Skills

- Acquisition and interpretation of data and testing hypotheses.
- Interpretation of datasheets and industry standards.
- Ability to design and construct circuits or systems.
- Implementation of algorithms as software code.
- Use of commercial software tools to analyse, design and simulate engineering systems.
- Recognise risks and identify safe working practices.
- Ability to write programs in at least one common programming language.
- Preparation of technical reports.

Professional Skills Development

- Communication of scientific material and arguments in written and oral formats.
- Recognise professional and ethical issues in the use of technology and identify appropriate ethical, professional and legal practices.
- Recognise issues of leadership and responsibility.
- Adoption of appropriate roles in group activities.
- Ability to interact with professionals from other disciplines.
- Ability to make decisions in complex and unpredictable situations.
- Ability to plan work in terms of time-plans and deliverables.
- Independent learning ability required for continuing professional development.

Entry Requirements	
Academic Requirement	Minimum A*AA overall to include A* in Mathematics and A in Physics. Relevant subjects for the remaining A-level include: Applied ICT, Biology, Chemistry, Computer Science, Computing, Design and Technology, Economics, Electronics, English Literature, Further Mathematics, Geography, History, ICT, Languages (Classical and Modern), Music, Music Technology, Statistics and Technology.
Non-academic Requirements	None
Home/EU/international students will be invited to attend an interview.	
English Language Requirement	IELTS 6.5 with a minimum of 6.0 in each element or equivalent
The programme's competency standards document can be found at: http://www3.imperial.ac.uk/electricalengineering/teaching/undergraduate/teaching	
Learning & Teaching Strategy	
Scheduled Learning & Teaching Methods	<ul style="list-style-type: none"> • Lectures • Problem solving classes • Tutorial sessions
E-learning & Blended Learning Methods	<ul style="list-style-type: none"> • Hardware laboratory • Software laboratory
Project and Placement Learning Methods	<ul style="list-style-type: none"> • Group projects • Individual projects • Industrial placement
Assessment Strategy	
Assessment Methods	<ul style="list-style-type: none"> • Written examinations • Coursework software or hardware deliverable • Oral and poster presentations • Reports
Academic Feedback Policy	
<p>The progress of first year students is evaluated via a Christmas and Easter test. Students receive feedback on these through their tutorials and study groups. Written feedback is made available on reports while in-situ comments give feedback on presentations.</p> <p>Two feedback strategies are applied to coursework based modules in the 3rd year. One involves comments on the individual reports while the second approach involves feedback sessions in class that discuss the solutions.</p>	

Feedback on exams is made available via the on-line publication of the answers with annotations that highlight the strength and weakness of understanding of the cohort on particular aspects of the module covered in the exams.

Re-sit Policy

The College's Policy on Re-sits is available at: www.imperial.ac.uk/registry/exams/resit

The departmental re-sit policy is available in the Awards of Honours document at:
<https://workspace.imperial.ac.uk/electricalengineering/Public/HonoursSchemes/EEE%20Scheme%20for%20Award%20of%20Honours%202015-16.pdf>

Mitigating Circumstances Policy

The College's Policy on Mitigating Circumstances is available at: www.imperial.ac.uk/registry/exams.
The department's policy on the registering of Mitigating Circumstances is available at:
<http://www3.imperial.ac.uk/electricalengineering/teaching/undergraduate/examinations/illnessug>

Assessment Structure

Marking Scheme

Year 1

In order to progress to Part II, students are normally required to achieve the following minimum marks:

1. 40% in each of the examined modules.
2. 40% in the aggregate of the practical work.

Year 2

In order to progress to Part III of MEng, students are normally required to achieve the following minimum marks:

3. 40% in each of the examined modules (excl. Maths).
4. 50% in mathematics.
5. 50% in the exam aggregate.
6. 40% in the aggregate of the practical work.
7. No SQT (supplementary qualifying test).

Compensation: 1 module (excluding maths) with a mark in the range 30% to 40% can be compensated (considered as a pass), *if* the exam aggregate is > 45%.

Year 3

In order to progress to Part IV of the MEng (including the Year Abroad stream) candidates must achieve 40% in aggregate in Part III. There is no pass mark for individual modules, only for the module aggregate.

Year 4

To obtain an honours degree, a candidate must obtain at least 40% in Part IV, normally by obtaining at least 40% in each of the elements: group project or industrial placement, module aggregate and individual project. There is no pass mark for individual modules, only for the module aggregate.

Final Assessment and Honours Classification

The marks from each Part are combined using the weightings defined in the Awards of Honours document.

First class honours:	A > 70%
Second class honours (upper division):	70 > B > 60%
Second class honours (lower division):	60 > C > 50%
Third class honours:	50 > D > 40%
Fail:	40 > E

Year	% Year Weighting	Module	% Module Weighting
Year One	11.1%	Analysis of Circuits	7.5%
		Digital Electronics 1	7.5%
		Introduction to Signals and Communications	7.5%
		Software Engineering 1: Introduction to Computing	7.5%
		Software Engineering 1: Algorithms and Data Structures	7.5%
		Introduction to Computer Architecture and Systems	7.5%
		Mathematics I (E-stream and I-stream)	15%
		User-centred Information Systems	7.5%
		Engineering Design and Practice	7.5%
		EIE 1st Year Computing Lab	10%
		EIE 1st Year Electronics Lab	5%
		EIE1 Group Design Project	10%
Year Two	22.2%	Databases	6%
		Computer Networks and Distributed Systems	6%
		Digital Electronics II	6%
		Communication Systems	6%
		Signals and Linear Systems	6%
		Mathematics II	14%
		Algorithms and Complexity	4%
		Software Engineering 2: Object-oriented Software Engineering	6%
		Computer Architecture II	6%
		Language Processors	6%
		Feedback Systems	4%
		EIE2 Computing Lab	10%
		EIE2 Electronics Lab	12%
EIE2 Architecture Workshop	8%		

Year	% Year Weighting	Module	% Module Weighting
Year Three	33.3%	Eight modules from elective group (A) to include: at least ONE module from the Department of Electrical and Electronic Engineering, at least ONE from the Department of Computing and exactly ONE non-technical option from the Humanities, Language or Business schools.	9.625%
		ONE module from electric group (B): Industry led group project or Industrial placement.	23%
Year Four	33.4%	EIE4 MEng Project	40%
		SEVEN modules from elective group (C) to include: at least ONE module from the Department of Electrical and Electronic Engineering, at least ONE from the Department of Computing and exactly ONE non-technical option from the Humanities, Language or Business schools.	8.57%*
		Candidates may choose an additional (eighth) option from elective group (C).	*

* Where candidates chose to take an additional eighth module the % module weighting for the other seven modules from elective group (C) would be weighted at 8% each.

Indicative Module List												
Code	Title	Core/ Elective	Year	L&T Hours	Ind. Study Hours	Place- ment Hours	Total Hours	% Written Exam	% Course- work	% Practi- cal	FHEQ Level	ECTS
EE1-01	Analysis of Circuits	CORE	1	35	90	0	125	100%	0%	0%	4	5
EE1-02	Digital Electronics 1	CORE	1	35	90	0	125	100%	0%	0%	4	5
EE1-06	Introduction to Signals and Communications	CORE	1	35	90	0	125	100%	0%	0%	4	5
EE1-07	Software Engineering 1: Introduction to Computing	CORE	1	20	105	0	125	0%	100%	0%	4	5
EE1-08	Software Engineering 1: Algorithms and Data Structures	CORE	1	20	80	0	100	100%	0%	0%	4	4
EE1-09	Introduction to Computer Architecture and Systems	CORE	1	25	75	0	100	100%	0%	0%	4	4
EE1-10	Mathematics I (E-stream and I-stream)	CORE	1	90	160	0	250	100%	0%	0%	4	10
EE1-12	User-centred Information Systems	CORE	1	25	100	0	125	0%	100%	0%	4	5
EE1-13	Engineering Design and Practice	CORE	1	20	105	0	125	0%	50%	50%	4	5
EE1-ILABC	EIE 1st Year Computing Lab	CORE	1	40	10	0	50	0%	100%	0%	4	2
EE1-ILABE	EIE 1st Year Electronics Lab	CORE	1	100	25	0	125	0%	50%	50%	4	5
EE1-IPRJ	EIE1 Group Design Project	CORE	1	15	110	0	125	100%	0%	0%	4	5

Indicative Module List												
Code	Title	Core/ Elective	Year	L&T Hours	Ind. Study Hours	Place- ment Hours	Total Hours	% Written Exam	% Course- work	% Practi- cal	FHEQ Level	ECTS
CO526	Databases	CORE	2	27	73	0	100	85%	15%	0%	7	4
CO527	Computer Networks and Distributed Systems	CORE	2	27	73	0	100	85%	15%	0%	7	4
EE2-01	Digital Electronics II	CORE	2	24	76	0	100	100%	0%	0%	5	4
EE2-04	Communication Systems	CORE	2	24	76	0	100	100%	0%	0%	5	4
EE2-05	Signals and Linear Systems	CORE	2	24	76	0	100	100%	0%	0%	5	4
EE2-08	Mathematics II	CORE	2	60	140	0	200	100%	0%	0%	5	8
EE2-10C	Algorithms and Complexity	CORE	2	17	58	0	75	100%	0%	0%	5	3
EE2-12	Software Engineering 2: Object-oriented Software Engineering	CORE	2	24	76	0	100	100%	0%	0%	5	4
EE2-13	Computer Architecture II	CORE	2	24	76	0	100	100%	0%	0%	5	4
EE2-15	Language Processors	CORE	2	24	76	0	100	100%	0%	0%	5	4
EE2-21	Feedback Systems	CORE	2	15	60	0	75	100%	0%	0%	5	3
EE2-ILABC	EIE2 Computing Lab	CORE	2	40	60	0	100	0%	100%	0%	5	3
EE2-ILABE	EIE2 Electronics Lab	CORE	2	90	60	0	150	0%	50%	50%	5	6

Indicative Module List												
Code	Title	Core/ Elective	Year	L&T Hours	Ind. Study Hours	Place- ment Hours	Total Hours	% Written Exam	% Course- work	% Practi- cal	FHEQ Level	ECTS
EE2-IPRJ	EIE2 Architecture Workshop	CORE	2	30	70	0	100	0%	100%	0%	5	4
EE3-IPRJ	EIE3 BEng Project	CORE	3	80	370	0	450	0%	80%	20%	6	18
EE3-03	Communication Systems	ELECTIVE (A)	3	20	130	0	150	100%	0%	0%	6	6
EE3-05	Digital System Design	ELECTIVE (A)	3	20	130	0	150	0%	100%	0%	6	6
EE3-07	Digital Signal Processing	ELECTIVE (A)	3	20	130	0	150	100%	0%	0%	6	6
EE3-08	Advanced Signal Processing	ELECTIVE (A)	3	20	130	0	150	0%	100%	0%	6	6
EE3-09	Control Engineering	ELECTIVE (A)	3	20	130	0	150	100%	0%	0%	6	6
EE3-10	Mathematics for Signals and Systems	ELECTIVE (A)	3	20	130	0	150	100%	0%	0%	6	6
EE3-16	Artificial Intelligence	ELECTIVE (A)	3	20	130	0	150	100%	0%	0%	6	6
EE3-17	Communication Networks	ELECTIVE (A)	3	20	130	0	150	100%	0%	0%	6	6
EE3-19	Real-time Digital Signal Processing	ELECTIVE (A)	3	20	130	0	150	0%	100%	0%	6	6
EE3-18	Microwave Technology	ELECTIVE (A)	3	20	130	0	150	100%	0%	0%	6	6
EE3-22	High Level Programming	ELECTIVE (C)	3	20	130	0	150	0%	100%	0%	6	6
EE3-23	Introduction to Machine Learning	ELECTIVE (C)	3	20	130	0	150	0%	100%	0%	6	6

Indicative Module List												
Code	Title	Core/ Elective	Year	L&T Hours	Ind. Study Hours	Place- ment Hours	Total Hours	% Written Exam	% Course- work	% Practi- cal	FHEQ Level	ECTS
EE3-24	Embedded Systems	ELECTIVE (C)	3	20	130	0	150	0%	100%	0%	6	6
CO312	Advanced Databases	ELECTIVE (A)	3	27	98	0	125	82%	18%	0%	6	4
CO316	Computer Vision	ELECTIVE (A)	3	27	73	0	100	85%	15%	0%	6	4
CO317	Graphics	ELECTIVE (A)	3	27	73	0	100	85%	15%	0%	6	4
CO318	Custom Computing	ELECTIVE (A)	3	27	73	0	100	85%	15%	0%	6	4
CO333	Robotics	ELECTIVE (A)	3	27	73	0	100	85%	15%	0%	6	4
CO337	Simulation and Modelling	ELECTIVE (A)	3	27	73	0	100	85%	15%	0%	6	4
CO341	Introduction to Bioinformatics	ELECTIVE (A)	3	27	73	0	100	85%	15%	0%	6	4
CO343	Operations Research	ELECTIVE (A)	3	27	85.5	0	112.5	85%	15%	0%	6	4.5
CO347	Distributed Algorithms	ELECTIVE (A)	3	27	73	0	100	85%	15%	0%	6	4
CO528	Concurrent Programming	ELECTIVE (A)	3	27	73	0	100	85%	15%	0%	7	4
N/A	Business for Professional Engineers & Scientists	ELECTIVE (A/C)	3/4	Various			150	Various				6
N/A	Horizons	ELECTIVE (A/C)	3/4	Various			150	Various				6

Indicative Module List												
Code	Title	Core/ Elective	Year	L&T Hours	Ind. Study Hours	Place- ment Hours	Total Hours	% Written Exam	% Course- work	% Practi- cal	FHEQ Level	ECTS
EE3-IP	Third Year Industrial Placement	ELECTIVE (B)	4	20	0	1030	1050	0%	80%	20%	6	42
EE3-JPRJ	EIE3 MEng Group Project	ELECTIVE (B)	4	20	1030	0	1050	0%	80%	20%	6	18
EE4-JPRJ	EIE4 MEng Project	CORE	4	100	500	0	600	0%	80%	20%	7	24
BE3-HHCARD	Computer Assistive and Rehabilitative Devices	ELECTIVE (C)	4	30	120	0	150	0%	60%	40%	6	6
BE9-MHNCL	Human Neuromechanical Control and Learning	ELECTIVE (C)	4	30	107.5	0	137.5	100%	0%	0%	7	5.5
CO331	Network and Web Security	ELECTIVE (C)	4	27	98	0	125	17.5%	0%	82.5%	6	5
CO332	Advanced Computer Architecture	ELECTIVE (C)	4	27	85.5	0	112.5	85%	15%	0%	6	4.5
CO395	Machine Learning	ELECTIVE (C)	4	27	73	0	100	70%	30%	0%	6	4
CO417	Advanced Computer Graphics	ELECTIVE (C)	4	27	85.5	0	112.5	85%	15%	0%	7	4.5
CO421	Computational Neurodynamics	ELECTIVE (C)	4	27	85.5	0	112.5	85%	15%	0%	7	4.5
CO422	Computational Finance	ELECTIVE (C)	4	27	85.5	0	112.5	85%	15%	0%	7	4.5
CO429	Parallel Algorithms	ELECTIVE (C)	4	27	85.5	0	112.5	85%	15%	0%	7	4.5
CO436H	Performance Analysis	ELECTIVE (C)	4	13.5	42.8	0	56.3	85%	15%	0%	7	2.25

Indicative Module List												
Code	Title	Core/ Elective	Year	L&T Hours	Ind. Study Hours	Place- ment Hours	Total Hours	% Written Exam	% Course- work	% Practi- cal	FHEQ Level	ECTS
CO438	Complexity	ELECTIVE (C)	4	27	85.5	0	112.5	85%	15%	0%	7	4.5
CO474	Argumentation and Multi-agent Systems	ELECTIVE (C)	4	27	85.5	0	112.5	85%	15%	0%	7	4.5
CO475	Software Engineering for Industry	ELECTIVE (C)	4	27	85.5	0	112.5	85%	15%	0%	7	4.5
CO477	Computing for Optimal Decisions	ELECTIVE (C)	4	27	85.5	0	112.5	85%	15%	0%	7	4.5
CO493	Intelligent Data and Probabilistic Inference	ELECTIVE (C)	4	27	85.5	0	112.5	85%	15%	0%	7	4.5
EE4-01	Advanced Communication Theory	ELECTIVE (C)	4	25	100	0	125	55%	45%	0%	7	6
EE4-06	Optical Communication	ELECTIVE (C)	4	25	100	0	125	100%	0%	0%	7	6
EE4-07	Coding Theory	ELECTIVE (C)	4	25	100	0	125	100%	0%	0%	7	6
EE4-08	Digital Image Processing	ELECTIVE (C)	4	25	100	0	125	100%	0%	0%	7	6
EE4-10	Probability and Stochastic Processes	ELECTIVE (C)	4	25	100	0	125	85%	15%	0%	7	6
EE4-12	Digital Signal Processing and Digital Filters	ELECTIVE (C)	4	25	100	0	125	100%	0%	0%	7	6
EE4-13	Adaptive Signal Processing and machine Intelligence	ELECTIVE (C)	4	25	100	0	125	0%	100%	0%	7	6
EE4-14	Speech Processing	ELECTIVE (C)	4	25	100	0	125	100%	0%	0%	7	6
EE4-20	Full-Custom Integrated Circuit Design	ELECTIVE (C)	4	25	100	0	125	0%	100%	0%	7	6

Indicative Module List												
Code	Title	Core/ Elective	Year	L&T Hours	Ind. Study Hours	Place- ment Hours	Total Hours	% Written Exam	% Course- work	% Practi- cal	FHEQ Level	ECTS
EE4-23	Stability and Control of Non-linear Systems	ELECTIVE (C)	4	25	100	0	125	75%	25%	0%	7	6
EE4-24	Discrete-time Systems and Computer Control	ELECTIVE (C)	4	25	100	0	125	100%	0%	0%	7	6
EE4-25	Design of Linear Multivariable Control Systems	ELECTIVE (C)	4	25	100	0	125	100%	0%	0%	7	6
EE4-27	Systems Identification	ELECTIVE (C)	4	25	100	0	125	100%	0%	0%	7	6
EE4-29	Optimisation	ELECTIVE (C)	4	25	100	0	125	100%	0%	0%	7	6
EE4-40	Information Theory	ELECTIVE (C)	4	25	100	0	125	100%	0%	0%	7	6
EE4-45	Wavelets and Applications	ELECTIVE (C)	4	25	100	0	125	75%	25%	0%	7	6
EE4-46	Distributed Computation and Networks: A Performance Perspective	ELECTIVE (C)	4	25	100	0	125	0%	100%	0%	7	6
EE4-50	Sustainable Electrical Systems	ELECTIVE (C)	4	25	100	0	125	80%	20%	0%	7	6
EE4-51	Power System Economics	ELECTIVE (C)	4	25	100	0	125	80%	20%	0%	7	6
EE4-54	Predictive Control	ELECTIVE (C)	4	25	100	0	125	75%	25%	0%	7	6
EE4-60	Human-Centered Robotic	ELECTIVE (F)	4	25	100	0	125	0%	100%	0%	7	6
EE4-62	Machine Learning for Computer Vision	ELECTIVE (C)	4	25	100	0	125	0%	100%	0%	7	6

Indicative Module List												
Code	Title	Core/ Elective	Year	L&T Hours	Ind. Study Hours	Place- ment Hours	Total Hours	% Written Exam	% Course- work	% Practi- cal	FHEQ Level	ECTS
EE4-63	High Performance Computing for Engineers	ELECTIVE (C)	4	25	100	0	125	0%	100%	0%	7	6
EE4-66	Topics in Large Dimensional Data Processing	ELECTIVE (C)	4	25	100	0	125	100%	0%	0%	7	6
EE4-67	Mobile Healthcare and Machine Learning	ELECTIVE (F)	4	25	100	0	125	100%	0%	0%	7	6
EE4-68	Pattern Recognition	ELECTIVE (F)	4	25	100	0	125	100%	0%	0%	7	6
ME3-hDNVC	Design-led Innovation and New Venture Creation	ELECTIVE (C)	4	31	119	0	150	0%	70%	30%	6	6

Supporting Information

The Programme Handbook is available at:

<http://www3.imperial.ac.uk/electricalengineering/teaching/undergraduate>

The Module Handbook is available at:

<http://www3.imperial.ac.uk/electricalengineering/teaching/undergraduate>

The College's entry requirements for undergraduate programmes can be found at:

www.imperial.ac.uk/study/ug/apply/requirements/

The College's Quality & Enhancement Framework is available at:

www.imperial.ac.uk/registry/proceduresandregulations/qualityassurance

The College's Academic and Examination Regulations can be found at:

<http://www3.imperial.ac.uk/registry/proceduresandregulations/regulations>

Imperial College is an independent corporation whose legal status derives from a Royal Charter granted under Letters Patent in 1907. In 2007 a Supplemental Charter and Statutes was granted by HM Queen Elizabeth II. This Supplemental Charter, which came into force on the date of the College's Centenary, 8th July 2007, established the College as a University with the name and style of "The Imperial College of Science, Technology and Medicine".

<http://www.imperial.ac.uk/admin-services/secretariat/college-governance/charters-statutes-ordinances-and-regulations/>

Imperial College London is regulated by the Higher Education Funding Council for England (HEFCE)

<http://www.hefce.ac.uk/reg/of/>